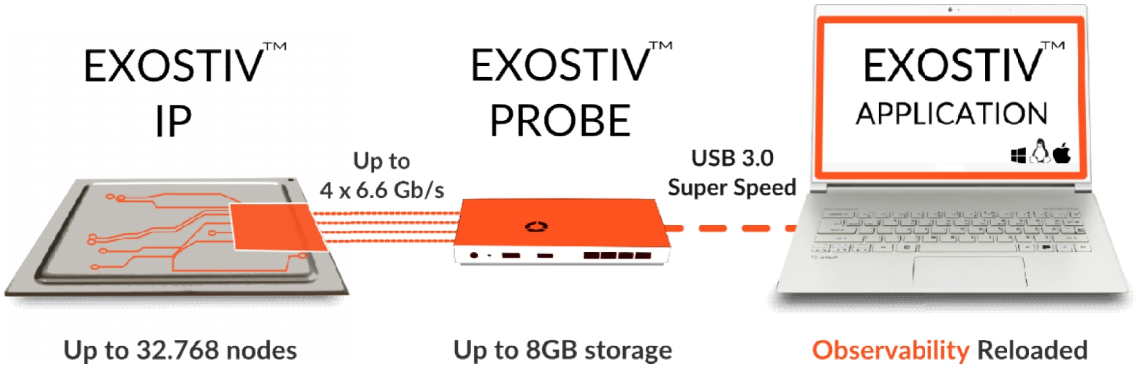
EXOSTIV™

EXOSTIV™ FPGA debug solution

EXOSTIV™ is the first FPGA debug solution that provides Gigabyte-range observability with a minimal footprint on the target chip resources. This innovative software uses a low profile IP directly inserted into the design. It combines large hardware bandwidth and external storage capacity to reach, collect and analyze FPGA signals at the speed of operation.

BENEFITS

- Dramatically increases observability:
  - Extended reach over the system logic.
  - Extended reach in time.
  - Signals are captured at the speed of operation.
- Preserves the FPGA I/O and memory resources.
- Seamlessly processes large debug databases.



EXOSTIV™ Probe uses the FPGA's multi-gigabit transceivers (MGT) to flow captured data to an external memory, providing up to 8 Gigabyte of debug data storage.

EXOSTIV™ Application includes MYRIAD™ waveform viewer, the industry's first waveform viewer capable of handling terabytes of digital and analog waveform data.

EXOSTIV™ IP supports repeating captures of up to 32.768 internal nodes simultaneously at the FPGA's speed of operation (16 data sets x 2.048 bits). EXOSTIV™ IP provides dynamic multiplexer control to capture even more data sets without the need to recompile. Dynamic ON/OFF controls of data sets preserve the MGT's bandwidth for deeper captures.

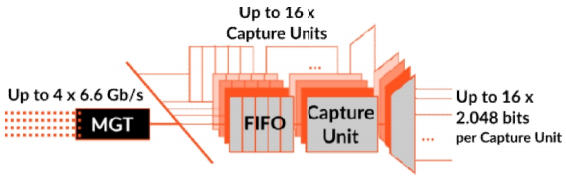
EXOSTIV™ minimizes the time spent on FPGA debug.

FEATURES

- Xilinx devices support from Series 7*
- Requires Vivado software for IP synthesis
- Up to 8 GB external storage
- Up to 4 x 6.6 Gb/s** MGT connections
  - SFP / SFP+ cages
  - HDMI connector
  - SDI, PCIe, SATA, FMC (with adapters)
- MYRIAD™ waveform viewer (TB-capable)
- USB 3.0 connection with PC

- Configurable embedded instrumentation IP
- RTL or post-synthesis IP insertion
- Complex triggering options
- Dynamic data sets multiplexing
- Transitional storage
- Data qualification / data filtering

EXOSTIV IP



* Contact us for devices & manufacturers support roadmap.
** Roadmap for 12.5 Gb/s and beyond.