

		TOTAL (device)	Used by EXOSTIV IP	EXOSTIV IP / FPGA
1 x CU x 512 nodes with 1k buffer - Complex trigger logic				
Arria 10	ALM	427,200	10,808	3%
10AX115S2F45I 1SG	Registers	1,708,800	17,452	1%
	Total memory bits	55,574,528	537,600	1%
1 x CU x 1024 nodes with 1k buffer - Simple trigger logic				
Arria 10	ALM	427,200	12,038	3%
10AX115S2F45I 1SG	Registers	1,708,800	20,753	1%
	Total memory bits	55,574,528	1,070,080	2%
1 x CU x 2048 nodes with 1k buffer - Simple trigger logic				
Arria 10	ALM	427,200	21,579	5%
10AX115S2F45I 1SG	Registers	1,708,800	37,600	2%
	Total memory bits	55,574,528	2,135,040	4%
1 x CU x 20480 nodes with 1k buffer - Complex trigger logic				
Arria 10	ALM	427,200	35,588	8%
10AX115S2F45I 1SG	Registers	1,708,800	57,441	3%
	Total memory bits	55,574,528	2,135,040	4%