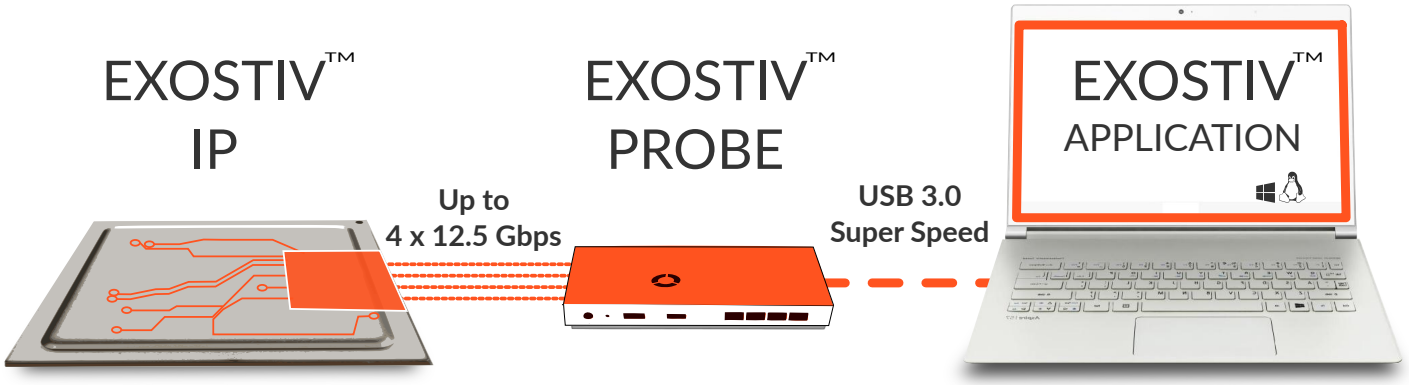
EXOSTIV LABS

FPGA Debug Reloaded

EXOSTIV™

FPGA Debug Solution



Up to 32,768 nodes | **Up to 8GB storage** | **Visibility Reloaded**

EXOSTIV™ Probe uses the FPGA's multi gigabit transceivers to flow captured data to an external memory, providing up to 8 Gigabyte of storage for debug data.

EXOSTIV™ Application includes MYRIAD™, the industry's first waveform viewer capable of handling terabytes of digital and analog waveform data.

EXOSTIV™ IP supports repeating captures of up to 32,768 internal nodes simultaneously at the FPGA's speed of operation. EXOSTIV™ IP provides dynamic multiplexer control to capture even more data sets and limit the need to recompile FPGA.
Dynamic ON/OFF controls of data sets preserve the MGT's bandwidth for deeper captures.

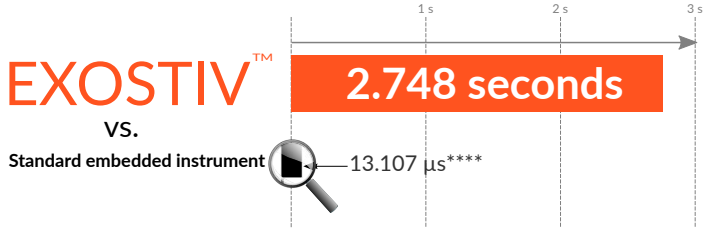
FEATURES

- Xilinx devices support: Zynq, Series 7 & Ultrascale(+)*
- Intel devices support: Series 10, Cyclone V & Stratix V*
- Requires FPGA vendor software for IP synthesis
- Up to 8 GB external storage
- Up to 4 x 12.5 Gbps MGT connections
  - SFP / SFP+ / QSFP / QSFP+ cages
  - HDMI, mini- and micro-HDMI connector
  - FMC and others (with adapter)
- MYRIAD™ waveform viewer (TB-capable)
- USB 3.0 connection with PC

- Configurable embedded instrumentation IP
- RTL or post-synthesis IP insertion
- Complex triggering options
- Dynamic data sets multiplexing
- Transitional storage
- Data qualification / data filtering

*Contact us for full devices support roadmap

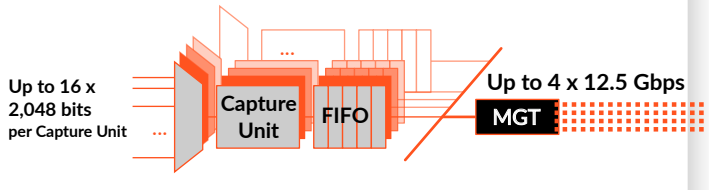
EXAMPLE OF OBSERVABLE OPERATING TIME***



*** Observation of **100 bits at 250 MHz.**
**** Typically using **40 kB** of FPGA memory

EXOSTIV™ Probe

EXOSTIV™ IP



"The ability to see inside the chip is the way to build better design.

www.exostivlabs.com